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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/813,157	03/31/2004	Youn-joon Sung	030681-642	4476

21839 7590 04/30/2008  
BUCHANAN, INGERSOLL & ROONEY PC  
POST OFFICE BOX 1404  
ALEXANDRIA, VA 22313-1404

EXAMINER
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GOLUB, MARCIA A

ART UNIT	PAPER NUMBER
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2828

NOTIFICATION DATE	DELIVERY MODE
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04/30/2008

ELECTRONIC

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

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ADIPFDD@bipc.com



## DETAILED ACTION

### *Response to Arguments*

Applicant's arguments have been fully considered but they are not persuasive.

Regarding applicant's argument that the upper electrode does not contact the contact hole, buried layer and protective layer, the examiner points out that Fig 1 of Kozaki illustrates the upper electrode to be in contact with contact hole and protective layer. Furthermore, the figure shows that the upper electrode contacts lower electrode which in turn contacts buried layer, therefore the upper electrode is in contact with the buried layer through the lower electrode.

### *Claim Rejections - 35 USC § 102*

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

**Claim 1-23** are rejected under 35 U.S.C. 102(b) as being anticipated by Kozaki (US 2002/00536760).

Fig 1 of Kozaki discloses a laser diode comprising:

1. “a substrate [101];  
a lower material layer [102-105] formed on the substrate [101];  
a resonance layer [106-109] formed on the lower material layer [102-105];  
an upper material layer [110-111] formed on the resonance layer [102-105] and having a ridge at the top;  
a buried layer [162] formed on the upper material layer [110-111] and having a contact hole corresponding to the ridge of the upper material layer; (paragraph 0232)  
a protective layer [164] formed on the buried layer [162] and having a material different from the material of the buried layer, and having an opening corresponding to the contact hole of the buried layer (paragraph 0173); and  
an upper electrode [122] formed on the protective layer [164] to contact an upper surface of the ridge through the contact hole,

wherein the upper surface of the ridge is planar (there are no curved surfaces on top of the ridge)

such that the upper electrode [122] is in contact with the contact hole, buried layer [162], and protective layer [164].” The buried layer is in contact with the upper electrode through the lower electrode layer.

2. wherein the lower material layer includes: a first compound semiconductor layer [103] stacked on the substrate [101]; and a lower cladding layer [105] stacked on the first compound semiconductor layer [103].

3. wherein the first compound semiconductor layer [103] is an n-GaN based group III-V nitride semiconductor layer (paragraphs 0065 and 0150).

4. wherein the lower cladding layer [105] is an n-GaN/AlGaN layer (paragraph 0154).

5. wherein the resonance layer further includes:

a lower waveguide layer [106] stacked on the lower cladding layer [105] and having a refractive index larger than that of the lower cladding layer (paragraph 0037);

an active layer [107] stacked on the upper surface of the lower waveguide layer [106] to generate a laser beam; and

an upper waveguide layer [109] stacked on the active layer [107].

6. wherein the refractive indexes of the upper and lower waveguide layers are lower than the refractive index of the active layer (paragraph 0037).

7. wherein the active layer [107] is a GaN based group III-V nitride compound semiconductor layer of  $\text{In}_x\text{Al}_y\text{Ga}_{1-x-y}\text{N}$  where  $0 \leq x \leq 1$ ,  $0 \leq y \leq 1$ , and  $x+y \leq 1$  (paragraph 0065).

8. wherein the upper material layer includes:

an upper cladding layer [110] stacked on the upper waveguide [109] layer and having a ridge (paragraph 0126) and a refractive index smaller than that of the upper waveguide layer (paragraph 0037);

a second compound semiconductor layer [111] formed on the ridge (paragraph 0166).

9. wherein the upper cladding layer [110] is a p-GaN/AlGaN layer (paragraph 0164).

10. wherein the second compound semiconductor layer [111] is a p-GaN based

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group III-V nitride semiconductor layer (paragraph 0166).

Regarding **claims 11-23**, the arguments applied above to the apparatus described with regards to claims 1-10 are applicable to the method claims as well. In addition paragraphs 0164-0174 recite the fabrication method steps as required by the claims.

### ***Conclusion***

This is an RCE of applicant's earlier application. All claims are drawn to the same invention claimed in the earlier application and could have been finally rejected on the grounds and art of record in the next Office action if they had been entered in the earlier application. Accordingly, **THIS ACTION IS MADE FINAL** even though it is a first action in this case. See MPEP § 706.07(b). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the date of this final action.

### ***Contact Info***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to MARCIA A. GOLUB whose telephone number is (571)272-8602. The examiner can normally be reached on M-F 9-6 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Minsun Harvey can be reached on 571-272-1835. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

/Marcia A. Golub/

/Minsun Harvey/  
Supervisory Patent Examiner, Art Unit 2828